

BGY785A 750 MHz, 18.5 dB gain push-pull amplifierRev. 05 — 22 March 2005Pr

Product data sheet

Product profile 1.

1.1 General description

Hybrid high dynamic range cascode amplifier module in a SOT115J package operating with a voltage supply of 24 V (DC).

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

- Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- Gold metallization ensures excellent reliability

1.3 Applications

CATV systems operating in the 40 MHz to 750 MHz frequency range

1.4 Quick reference data

Table 1:	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G _p	power gain	f = 50 MHz	18	18.5	19	dB
		f = 750 MHz	18.5	19.5	-	dB
I _{tot}	total current consumption (DC)	$V_B = 24 V$	<u>[1]</u> _	225	240	mA

[1] The module normally operates at $V_B = 24$ V, but is able to withstand supply transients up to 30 V.



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2. Pinning information

Table 2:	Pinning	
Pin	Description	Simplified outline Symbol
1	input	
2	common	1 3 5 7 9
3	common	
5	+V _B	
7	common	
8	common	
9	output	

3. Ordering information

Table 3: Ordering information							
Type number	Package	Package					
	Name	Description	Version				
BGY785A	-	rectangular single-ended package; aluminium flange; 2 vertical mounting holes; $2 \times 6-32$ UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads	SOT115J				

4. Limiting values

Table 4: Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Vi	RF input voltage		-	65	dBmV
T _{stg}	storage temperature		-40	+100	°C
T _{mb}	mounting base temperature		-20	+100	°C

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5. Characteristics

Table 5: Bandwidth 40 MHz to 750 MHz

 $V_B = 24 V$; $T_{case} = 30 \circ C$; $Z_S = Z_L = 75 \Omega$; unless otherwise specified.

Symbol	Parameter	Conditions	N	/lin Ty	p Max	Unit
G _p	power gain	f = 50 MHz	1	8 18	.5 19	dB
		f = 750 MHz	1	8.5 19	.5 -	dB
SL	slope cable equivalent	f = 40 MHz to 750 MHz	0	0.9	2	dB
FL	flatness of frequency response	f = 40 MHz to 750 MHz	-	±0	1 ±0.3	dB
s ₁₁	input return losses	f = 40 MHz to 80 MHz	2	0 30	-	dB
		f = 80 MHz to 160 MHz	1	8.5 29	5 -	dB
		f = 160 MHz to 320 MHz	1	7 28	-	dB
		f = 320 MHz to 640 MHz	1	5.5 26	-	dB
		f = 640 MHz to 750 MHz	1	4 21	-	dB
s ₂₂	output return losses	f = 40 MHz to 80 MHz	2	0 29	-	dB
		f = 80 MHz to 160 MHz	1	8.5 26	-	dB
		f = 160 MHz to 320 MHz	1	7 23	.5 -	dB
		f = 320 MHz to 640 MHz	1	5.5 22	-	dB
		f = 640 MHz to 750 MHz	1	4 24	-	dB
СТВ	composite triple beat	110 channels flat; V _o = 44 dBmV; measured at 745.25 MHz	-	-5	4.5 –53	dB
X _{mod}	cross modulation	110 channels flat; V _o = 44 dBmV; measured at 55.25 MHz	-	-5	7.5 –56	dB
CSO	composite second order distortion	110 channels flat; V _o = 44 dBmV; measured at 746.5 MHz	-	-6	2 –53	dB
d ₂	second order distortion		<u>[1]</u> _	-7	7 –65	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$	<u>[2]</u> 5	9 62	-	dBmV
F	noise figure	f = 50 MHz	-	4.5	5.5	dB
		f = 450 MHz	-	-	5.5	dB
		f = 550 MHz	-	-	5.5	dB
		f = 600 MHz	-	-	6	dB
		f = 750 MHz	-	6	7	dB
I _{tot}	total current consumption (DC)		[3] _	22	5 240	mA

[1] $f_p = 55.25 \text{ MHz}$; $V_p = 44 \text{ dBmV}$; $f_q = 691.25 \text{ MHz}$; $V_q = 44 \text{ dBmV}$; measured at $f_p + f_q = 746.5 \text{ MHz}$.

[2] Measured according to DIN45004B;

 $f_p = 740.25 \text{ MHz}; V_p = V_o; f_q = 747.25 \text{ MHz}; V_q = V_o - 6 \text{ dB}; f_r = 749.25 \text{ MHz}; V_r = V_o - 6 \text{ dB};$ measured at $f_p + f_q - f_r = 738.25 \text{ MHz}.$ [3] The module normally operates at $V_B = 24 \text{ V}$, but is able to withstand supply transients up to 30 V.

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
G _p	power gain	f = 50 MHz		18	18.5	19	dB
		f = 600 MHz		18.5	-	-	dB
SL	slope cable equivalent	f = 40 MHz to 600 MHz		0	-	1.5	dB
FL	flatness of frequency response	f = 40 MHz to 600 MHz		-	-	±0.3	dB
s ₁₁	input return losses	f = 40 MHz to 80 MHz		20	30	-	dB
		f = 80 MHz to 160 MHz		18.5	29.5	-	dB
		f = 160 MHz to 320 MHz		17	28	-	dB
		f = 320 MHz to 600 MHz		16	26	-	dB
S ₂₂	output return losses	f = 40 MHz to 80 MHz		20	29	-	dB
		f = 80 MHz to 160 MHz		18.5	26	-	dB
		f = 160 MHz to 320 MHz		17	23.5	-	dB
		f = 320 MHz to 600 MHz		16	22	-	dB
СТВ	composite triple beat	85 channels flat; V _o = 44 dBmV; measured at 595.25 MHz		-	-	-57	dB
X _{mod}	cross modulation	85 channels flat; $V_o = 44 \text{ dBmV}$; measured at 55.25 MHz		-	-	-59	dB
CSO	composite second order distortion	85 channels flat; V _o = 44 dBmV; measured at 596.5 MHz		-	-	-58	dB
d ₂	second order distortion		[1]	-	-	-70	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$	[2]	61	-	-	dBm∖
F	noise figure	f = 50 MHz		-	4.5	5.5	dB
		f = 450 MHz		-	-	5.5	dB
		f = 550 MHz		-	-	5.5	dB
		f = 600 MHz		-	-	6	dB
I _{tot}	total current consumption (DC)		[3]	-	225	240	mA

Table 6: Bandwidth 40 MHz to 600 MHz

[1] $f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV}; f_q = 541.25 \text{ MHz}; V_q = 44 \text{ dBmV}; \text{ measured at } f_p + f_q = 596.5 \text{ MHz}.$

[2] Measured according to DIN45004B;

 $f_p = 590.25 \text{ MHz}; V_p = V_o; f_q = 597.25 \text{ MHz}; V_q = V_o - 6 \text{ dB}; f_r = 599.25 \text{ MHz}; V_r = V_o - 6 \text{ dB}; \text{ measured at } f_p + f_q - f_r = 588.25 \text{ MHz}.$ [3] The module normally operates at $V_B = 24 \text{ V}$, but is able to withstand supply transients up to 30 V.

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
G _p	power gain	f = 50 MHz		18	18.5	19	dB
		f = 550 MHz		18.5	-	-	dB
SL	slope cable equivalent	f = 40 MHz to 550 MHz		0	-	1.5	dB
FL	flatness of frequency response	f = 40 MHz to 550 MHz		-	-	±0.3	dB
s ₁₁	input return losses	f = 40 MHz to 80 MHz		20	30	-	dB
		f = 80 MHz to 160 MHz		18.5	29.5	-	dB
		f = 160 MHz to 320 MHz		17	28	-	dB
		f = 320 MHz to 550 MHz		16	26	-	dB
S ₂₂	output return losses	f = 40 MHz to 80 MHz		20	29	-	dB
		f = 80 MHz to 160 MHz		18.5	26	-	dB
		f = 160 MHz to 320 MHz		17	23.5	-	dB
		f = 320 MHz to 550 MHz		16	22	-	dB
СТВ	composite triple beat	77 channels flat; V _o = 44 dBmV; measured at 547.25 MHz		-	-61	-60	dB
X _{mod}	cross modulation	77 channels flat; $V_0 = 44 \text{ dBmV}$; measured at 55.25 MHz		-	-61	-60	dB
CSO	composite second order distortion	77 channels flat; V _o = 44 dBmV; measured at 548.5 MHz		-	-67.5	-60	dB
d ₂	second order distortion		[1]	-	-	-72	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$	[2]	62	-	-	dBm∖
F	noise figure	f = 50 MHz		-	4.5	5.5	dB
		f = 450 MHz		-	-	5.5	dB
		f = 550 MHz		-	-	5.5	dB
I _{tot}	total current consumption (DC)		[3]	-	225	240	mA

Table 7: Bandwidth 40 MHz to 550 MHz

[1] $f_p = 55.25 \text{ MHz}$; $V_p = 44 \text{ dBmV}$; $f_q = 493.25 \text{ MHz}$; $V_q = 44 \text{ dBmV}$; measured at $f_p + f_q = 548.5 \text{ MHz}$.

[2] Measured according to DIN45004B;

 $f_p = 540.25 \text{ MHz}; V_p = V_o; f_q = 547.25 \text{ MHz}; V_q = V_o - 6 \text{ dB}; f_r = 549.25 \text{ MHz}; V_r = V_o - 6 \text{ dB};$ measured at $f_p + f_q - f_r = 538.25 \text{ MHz}.$

[3] The module normally operates at $V_B = 24$ V, but is able to withstand supply transients up to 30 V.

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
G _p	power gain	f = 50 MHz		18	18.5	19	dB
		f = 450 MHz		18.5	-	-	dB
SL	slope cable equivalent	f = 40 MHz to 450 MHz		0	-	1.5	dB
FL	flatness of frequency response	f = 40 MHz to 450 MHz		-	-	±0.3	dB
s ₁₁	input return losses	f = 40 MHz to 80 MHz		20	30	-	dB
		f = 80 MHz to 160 MHz		18.5	29.5	-	dB
		f = 160 MHz to 320 MHz		17	28	-	dB
		f = 320 MHz to 450 MHz		16	26	-	dB
S ₂₂	output return losses	f = 40 MHz to 80 MHz		20	29	-	dB
		f = 80 MHz to 160 MHz		18.5	26	-	dB
		f = 160 MHz to 320 MHz		17	23.5	-	dB
		f = 320 MHz to 450 MHz		16	22	-	dB
СТВ	composite triple beat	60 channels flat; V _o = 44 dBmV; measured at 445.25 MHz		-	-	-61	dB
X _{mod}	cross modulation	60 channels flat; V _o = 44 dBmV; measured at 55.25 MHz		-	-	-60	dB
CSO	composite second order distortion	60 channels flat; V _o = 44 dBmV; measured at 446.5 MHz		-	-	-61	dB
d ₂	second order distortion		[1]	-	-	-75	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$	[2]	64	-	-	dBm\
F	noise figure	f = 50 MHz		-	4.5	5.5	dB
		f = 450 MHz		-	-	5.5	dB
I _{tot}	total current consumption (DC)		[3]	-	225	240	mA

Table 8: Bandwidth 40 MHz to 450 MHz

 $[1] \quad f_p = 55.25 \text{ MHz}; \text{ } \text{V}_p = 46 \text{ } \text{dBmV}; \text{ } \text{f}_q = 391.25 \text{ } \text{MHz}; \text{ } \text{V}_q = 46 \text{ } \text{dBmV}; \text{ measured at } \text{f}_p + \text{f}_q = 446.5 \text{ } \text{MHz}.$

[2] Measured according to DIN45004B;

 $f_p = 440.25 \text{ MHz}; V_p = V_o; f_q = 447.25 \text{ MHz}; V_q = V_o - 6 \text{ dB}; f_r = 449.25 \text{ MHz}; V_r = V_o - 6 \text{ dB};$ measured at $f_p + f_q - f_r = 438.25 \text{ MHz}.$ [3] The module normally operates at $V_B = 24 \text{ V}$, but is able to withstand supply transients up to 30 V.

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6. Package outline

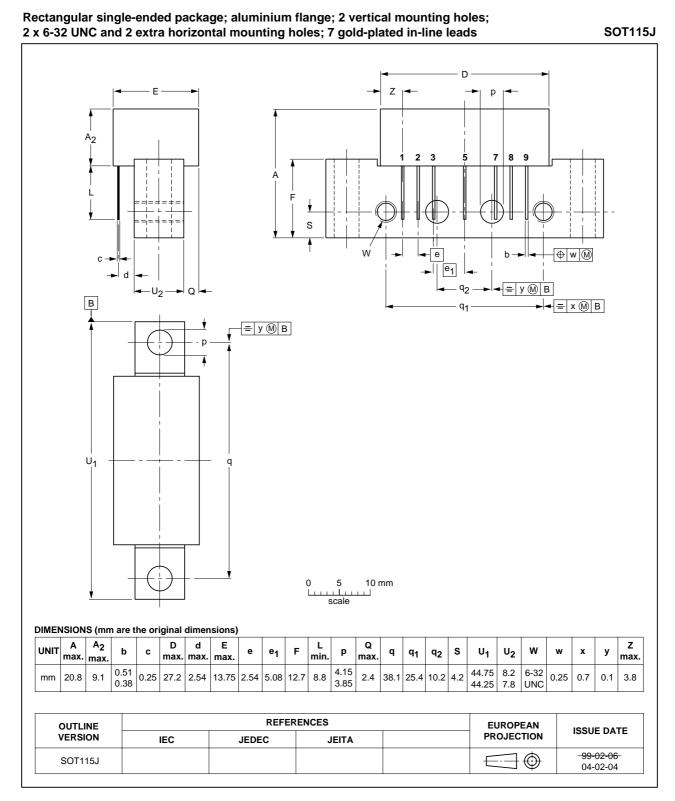


Fig 1. Package outline SOT115J

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7. Revision history

Table 9:Revision history

Document ID	Release date	Data sheet status	Change notice	Doc. number	Supersedes
BGY785A_5	20050322	Product data sheet	-	9397 750 14772	BGY785A_4
Modifications:		t of this data sheet has b n standard of Philips Sen		comply with the new	v presentation and
BGY785A_4	20011115	Product specification	-	9397 750 08808	BGY785A_3
BGY785A_3	19990330	Product specification	-	9397 750 05443	BGY785A_2
BGY785A_2	19970410	Product specification	-	9397 750 02142	n.a.

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8. Data sheet status

Level	Data sheet status [1]	Product status [2] [3]	Definition
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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[3] For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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